

AMENDMENTS TO THE SPECIFICATION

On page 16, bridging page 17, please delete the last paragraph and insert the following:

-- The signals from current sense 88 and voltage sense 92 are also applied to respective zero crossing detectors 100, 102. These produce a pulse whenever the respective signals cross zero. The pulse from detector 100 is applied to phase detection logic 104, which can include a counter that is started by that signal. The pulse from detector 102 is likewise applied to logic circuit ~~106~~ 104 and can be used to stop the counter. As a result, the count which is reached by the counter is a digital code on line ~~104~~ 106, which represents the difference in phase between the current and voltage. The size of this phase difference is also an indication of resonance. These signals can be used as part of a phase lock loop that cause the generator frequency to lock onto resonance, e.g., by comparing the phase delta to a phase set point in the DSP in order to generate a frequency signal to a direct digital synthesis (DDS) circuit 128 that drives the push-pull amplifier 78. --